

Parameters Subject to Change Without Notice

DESCRIPTION

The JW[®]1965A/JW1965B/JW1965C (JW1965X series) is a constant current LED driver which applies to single stage Boost power factor corrected LED drivers.

JW1965X series integrates high voltage power source, and can be supplied by output voltage directly, auxiliary winding and VCC capacitor are not needed.

Patented current sensing and digital compensation technics ensure a unit power factor and high accuracy output current. The critical conduction mode operation reduces the switching losses and increases the efficiency.

JW1965X series has multi-protection functions which largely enhance the safety and reliability of the system, including LED open protection, freewheeling diode open protection and over-temperature protection.

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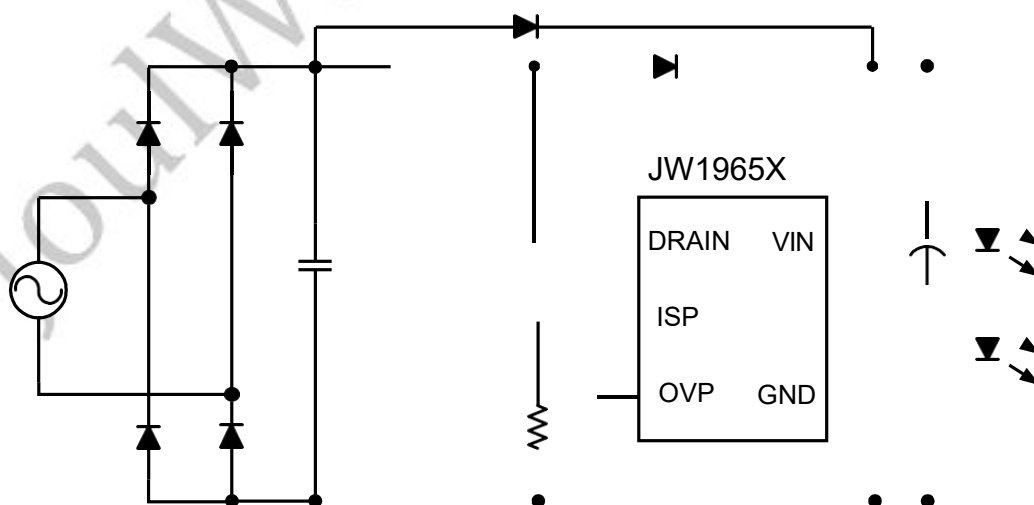
FEATURES

- No auxiliary winding and VCC capacitor
- Supplied from output voltage directly
- High current accuracy of line and load regulation
- Internal compensation PFC technics
- Critical conduction mode
- High efficiency over wide operating range
- LED open protection
- Freewheeling diode open protection
- Over-temperature protection
- SOP7 and DIP7 package

APPLICATIONS

- Non-isolation Offline LED driver

TYPICAL APPLICATION



ORDER INFORMATION

DEVICE ¹⁾	PACKAGE	TOP MARKING ²⁾
JW1965ASOPA#TRPBF	SOP7	JW1965A YWL L L L L
JW1965BSOPA#TRPBF	SOP7	JW1965B YWL L L L L
JW1965BDIPA#PBF	DIP7	JW1965B YWL L L L L
JW1965CDIPA#PBF	DIP7	JW1965C YWL L L L L

Notes :

- JW

1)

JW

2) Line1:
- # TRPBF

I

Product code of JWXXXX

Joulwatt LOGO
- PB Free
Tape and Reel(If " TR " is not shown, it means tube)

Package Code

Part No.

Line2:

Week code
Year code
- YWL L L L L

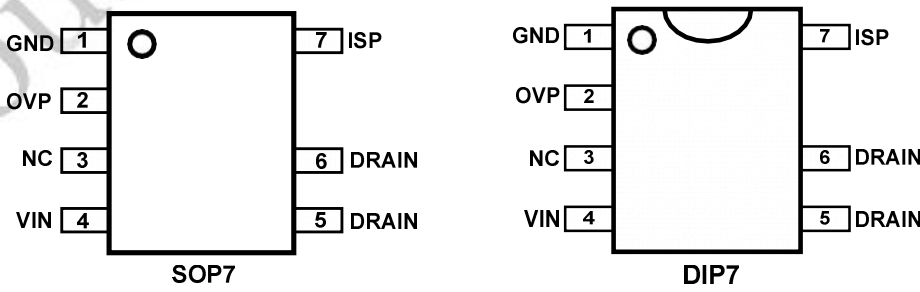
Lot number

DEVICE INFORMATION

JW1965ASOPA#TRPBF	650V	6.8Ω
JW1965BSOPA#TRPBF	600V	1.8Ω
JW1965CDIPA#PBF	650V	1Ω

PIN CONFIGURATION

TOP VIEW



ABSOLUTE MAXIMUM RATING¹⁾

VIN Voltage.....	V
ISP Voltage.....	V
Junction Temperature ^{2) 3)}	150°C
Lead Temperature.....	260°C
Storage Temperature.....	-65°C to +150°C
ESD Susceptibility (Human Body Model).....	kV

RECOMMENDED OPERATING CONDITIONS

VIN Voltage.....	V to 500V
Operating Junction Temp (T _J).....	-40°C to 125°C

THERMAL PERFORMANCE⁴⁾

	θ_{JA}	θ_{JC}	θ
SOP7.....	96.....	45°C/W	
DIP7.....	80.....	45°C/W	

Note :

- 1) Exceeding these ratings may damage the device.
- 2) Guarantees robust performance from -40°C to 150°C junction temperature. The junction temperature range specification is assured by design, characterization and correlation with statistical process controls.
- 3) Includes thermal protection that is intended to protect the device in overload conditions. Thermal protection is active when junction temperature exceeds the maximum operating junction temperature. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.
- 4) Measured on JESD51-7, 4-layer PCB.

ELECTRICAL CHARACTERISTICS

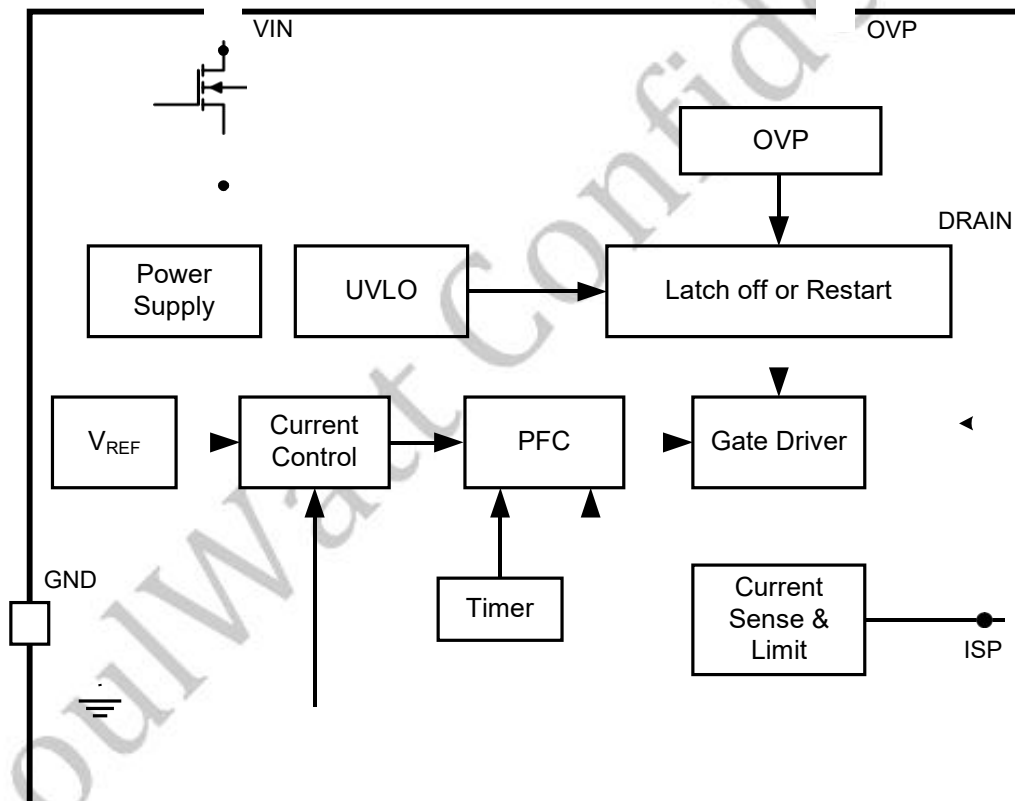
<i>T_A=25°C, unless otherwise stated.</i>							
Item		Symbol	Condition	Min.	Typ.	Max.	Units
VIN Start-Up Voltage		V _{IN_ST}			30		V
VIN Quiescent Current		I _{INQ}	GATE floating		320	450	μA
ISP Sample Reference		V _{REF}			100		mV
Minimum On Time of MOSFET ⁵⁾		T _{ON_MIN}			1.2		μS
Maximum On Time of MOSFET		T _{ON_MAX}			25		μS
Minimum Off Time of MOSFET ⁵⁾		T _{OFF_MIN}			2		μS
Maximum Off Time of MOSFET ⁵⁾		T _{OFF_MAX}			400		μS
Maximum Switch Frequency ⁵⁾		f _{MAX}			150		KHz
Switching Period of VINL ⁵⁾		T _{VINL}			65		μS
MOS R _{dson} ⁵⁾	JW1965A	R _{dson}	V _{gs} =10V		6.8		Ω
	JW1965B				1.8		
	JW1965C				1		
Breakdown Voltage	JW1965A	BV		650			V
	JW1965B			600			
	JW1965C			650			
ISP Over Voltage Protection Threshold		V _{ISP_MAX}			1.4		V
Vo Over Voltage Protection Threshold ⁵⁾		V _{O_OVP1}	V _{OVP} =2V		300		V
		V _{O_OVP2}	V _{OVP} =4.8V		580		V
		V _{O_OVP3}	V _{OVP} =0V		540		V
Thermal Protection Threshold ⁵⁾		T _{OTP}			150		°C

5) Guaranteed by design

PIN DESCRIPTION

Pin No.	Name	Description
1	GND	Chip ground.
2	OVP	Set OVP threshold.
3	NC	No Connection
4	VIN	High voltage input.
5,6	DRAIN	Drain of the MOSFET
7	ISP	Current sense.

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

The JW1965X series is a constant current LED driver which applies to non-isolation Boost LED system with power factor correction. JW1965X series can achieve excellent line and load regulation, high efficiency and low system cost with few peripheral components.

Start Up

JW1965X series is supplied by output voltage directly. When V_{IN} reaches V_{IN_ST} start up voltage (V_{IN_ST}), the chip begins to switch. Once V_{IN} is lower than V_{IN_ST} , JW1965X series stops switching.

Constant Current Control

The JW1965X series controls the output current from the information of the sensing resistor voltage. The output LED mean current can be calculated as:

$$I_{LED} = V_{REF} / R_{SNS}$$

Where

V_{REF} – ISP sample reference;

R_{SNS} – The sensing resistor connected between ISP and GND.

Critical Conduction Mode Operation

JW1965X series works in the Critical conduction mode of the inductor current. When the power MOSFET is turned on, the inductor current begins to increase from zero. The turn on time of the MOSFET can be calculated as:

$$T_{ON} = I_{PK} \times L / V_{IN}$$

Where,

L – inductance.

I_{PK} – peak current in one switch cycle.

V_{IN} – input voltage after rectification and filtering.

When the power MOSFET is turned off, the inductor current begins to decrease. The power

MOSFET turns on again when the inductor current is zero. The turn off time of the MOSFET can be calculated as:

$$T_{OFF} = I_{PK} \times L / (V_{OUT} - V_{IN})$$

Where,

V_{OUT} – output LED voltage.

And the inductance of the system can be calculated as:

$$L = V_{IN} \times (V_{OUT} - V_{IN}) / (f \times I_{PK} \times V_{OUT})$$

Where, f is the switching frequency of the step-down system.

LED Over Temperature Protection

When the internal temperature of the chip exceeds the thermal Protection Threshold (T_{OTP}), JW1965X series decreases LED current to help the chip cooling.

LED Open Protection

The OVP threshold (V_{O_OVP}) is set by the OVP pin. When V_O is higher than V_{O_OVP} , LED open protection is triggered and the chip stops switching for 800ms. The following table shows the V_{O_OVP} design guide:

OVP Pin	$V_{O_OVP}(V)$
Connected with 510 KΩ resistor	V_{O_OVP1}
Not connected	V_{O_OVP2}
Short connected	V_{O_OVP3}

PCB Design

When designing the PCB of the JW1965X series system, please follow the directions:

1. Make the area of the power loop as small as possible in order to reduce the EMI radiation.
2. The chip should be far away from the heating element, such as the power inductor.

REFERENCE DESIGN

This reference design is suitable for 15W non-isolated Boost LED driver, using JW1965A, with high efficiency, excellent line regulation.

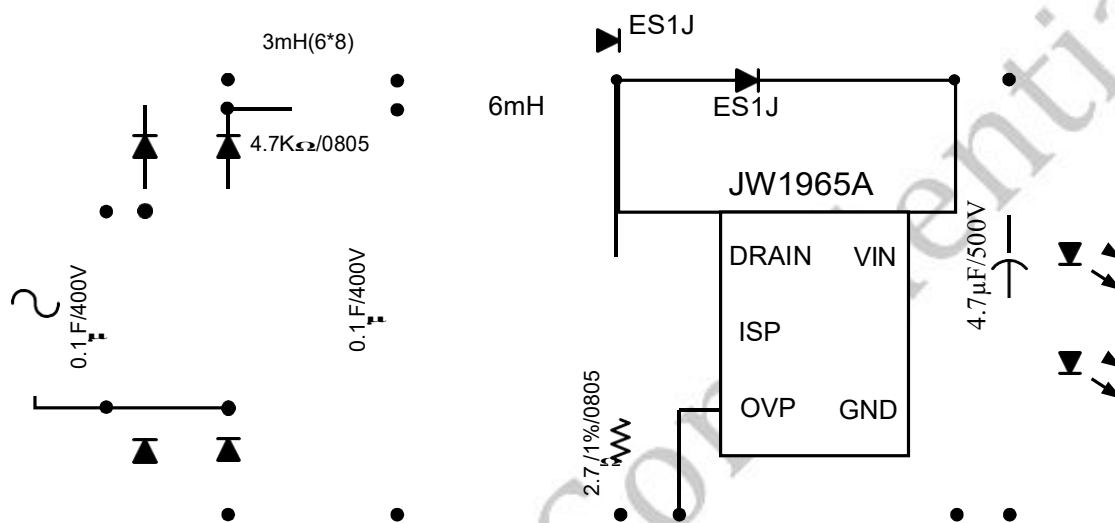
Reference :

V_{IN} : 90VAC~264VAC

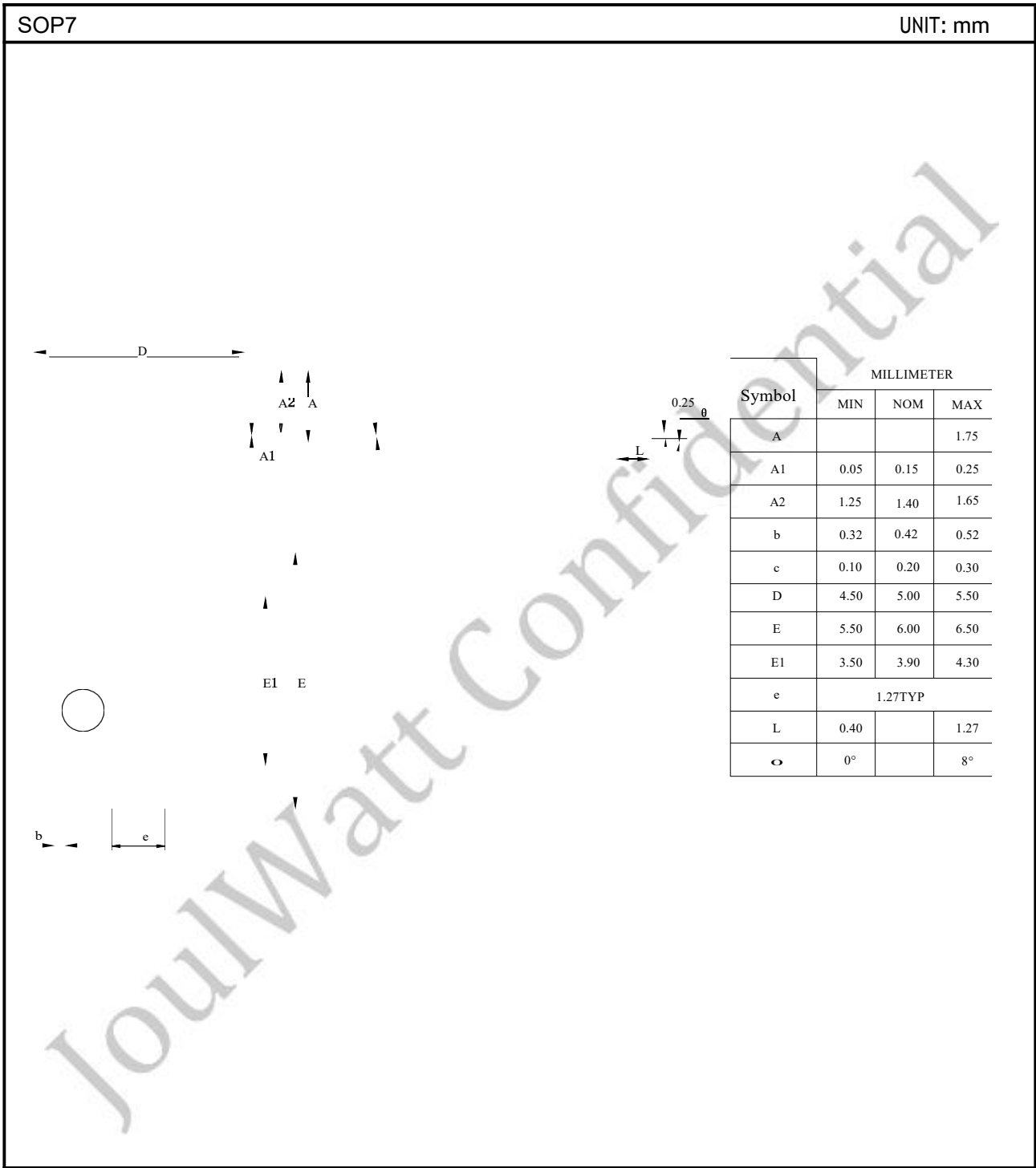
V_{OUT} : 420V

I_{OUT} : 30mA

PF: >0.9

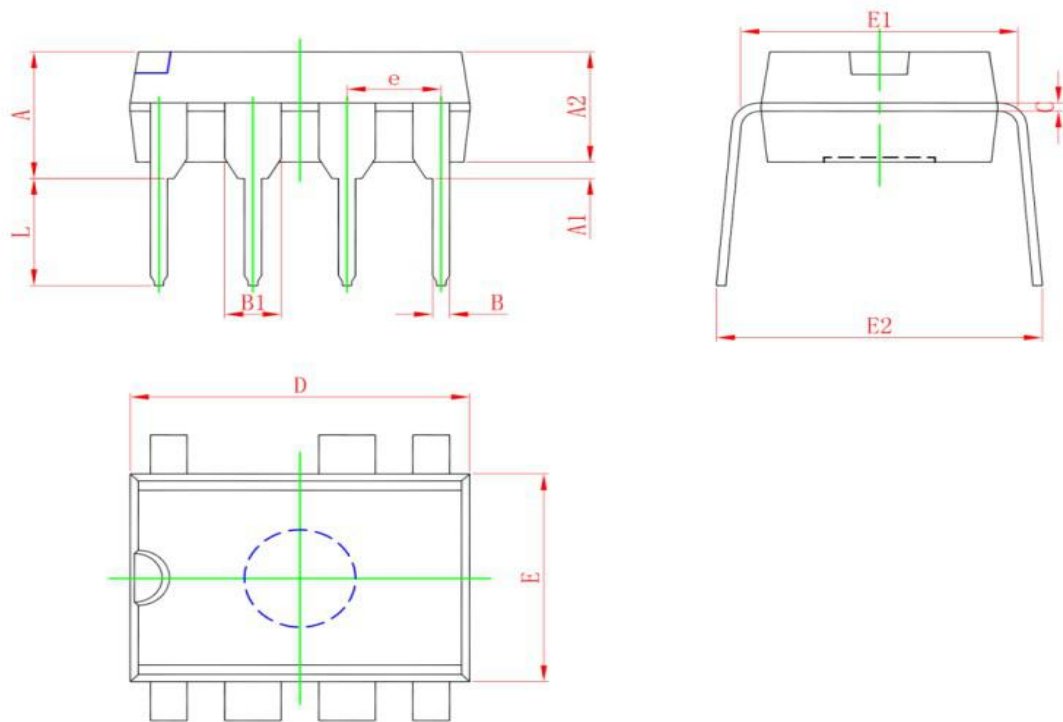


PACKAGE OUTLINE



DIP7

UNIT: mm



符号	Symbol		Dimension in Millimeters	
	Min	Max	Min	Max
A	3.710	4.310	0.146	0.170
A1	0.510		0.020	
A2	3.200	3.600	0.126	0.142
B	0.380	0.570	0.015	0.022
B1	1.524 (BSC)		0.060 (BSC)	
C	0.204	0.360	0.008	0.014
D	9.000	9.400	0.354	0.370
E	6.200	6.600	0.244	0.260
E1	7.320	7.920	0.288	0.312
e	2.540 (BSC)		0.100 (BSC)	
L	3.000	3.600	0.118	0.142
E2	8.400	9.000	0.331	0.354

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